

Towards ML-KEM & ML-DSA on OpenTitan

Amin Abdulrahman¹ Felix Oberhansl² Hoang Nguyen Hien Pham^{3,4} Jade Philipoom^{5,6}
Peter Schwabe^{1,7} Tobias Stelzer² Andreas Zankl^{2,8}

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¹Max Planck Institute for Security and Privacy (MPI-SP)
amin@abdulrahman.de, peter@cryptojedi.org

²Fraunhofer Institute for Applied and Integrated Security (AISEC)
firstname.lastname@aisec.fraunhofer.de

³BULL SAS ⁴Université Grenoble Alpes ⁵zeroRISC ⁶OpenTitan
nguyenhien.phamhoang@gmail.com jadep@zerorisc.com

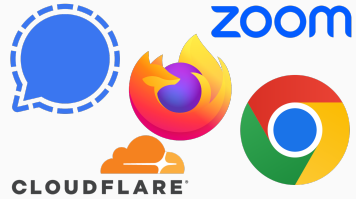
⁷Radboud University ⁸Technical University of Munich (TUM)

A Paradigm Shift



[Res21; App20; Com22; Goo22; Cor19; Clo25; Gmb; FSS20]

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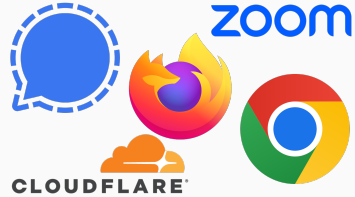
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RISQ-V: Tightly Coupled RISC-V Accelerators for Post-Quantum Cryptography

Tim Fritzmann¹, Georg Sigl¹ and Johanna Sepúlveda²

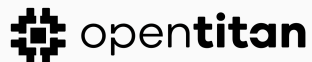
¹ Technical University of Munich, TUM Department of Electrical and Computer Engineering,
Chair of Security in Information Technology, Munich, Germany, tim.fritzmann,sigl@tum.de
² AIRBUS Defence and Space GmbH, Taufkirchen, Germany, johanna.sepulveda@airbus.com

[Res21; App20; Com22; Goo22; Cor19; Clo25; Gmb; FSS20]

*How efficient can we make **PQC***

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on an **existing pre-quantum platform***

*How efficient can we make **PQC**
on an **existing pre-quantum platform**
with modest hardware changes?*



- Open source silicon root of trust (RoT)
- Led by lowRISC, contributions from zeroRISC, Google, ETH Zurich, and many more
- Performs security critical operations
 - Hardware support for many cryptographic operations
 - Hardening against side-channel attacks
- Coming to, e.g., Google's 2025 Chromebooks [S025]

Target: OpenTitan

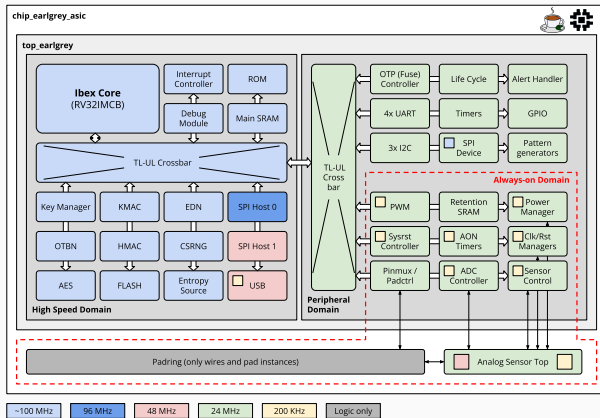


Figure 1: Overview OpenTitan Earl Grey [Ope23]

Target: OpenTitan

- Accelerator for SHA-3 and SHAKE

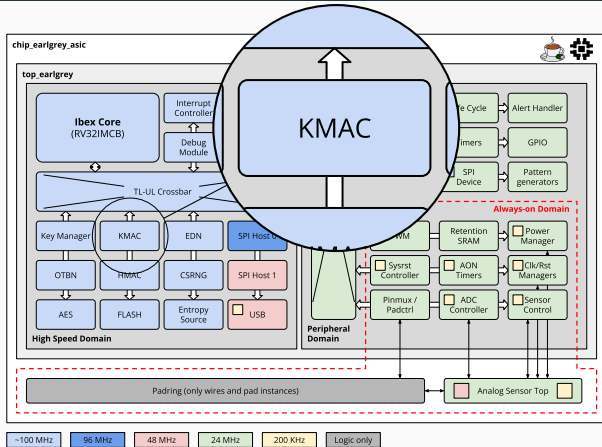


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Target: OpenTitan

- Accelerator for SHA-3 and SHAKE
- OpenTitan Big Number

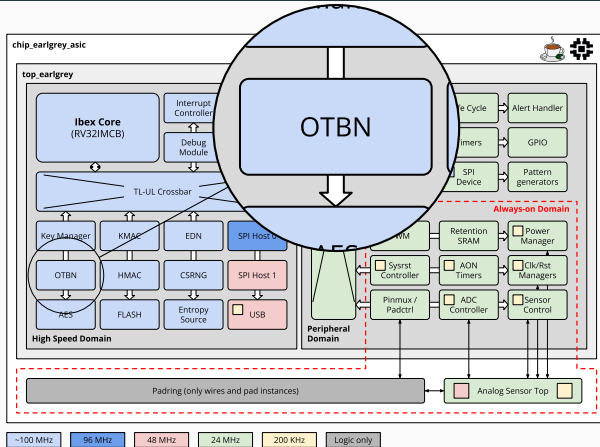


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Target: OpenTitan

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 - 32 256-bit registers

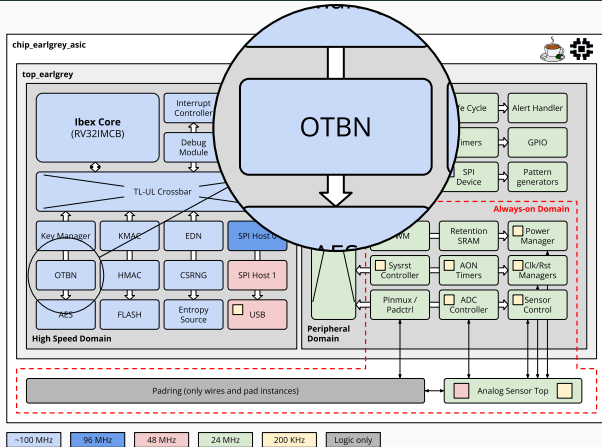


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Target: OpenTitan

- Accelerator for SHA-3 and SHAKE
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 - 32 256-bit registers
 - Reduced RV32I ISA

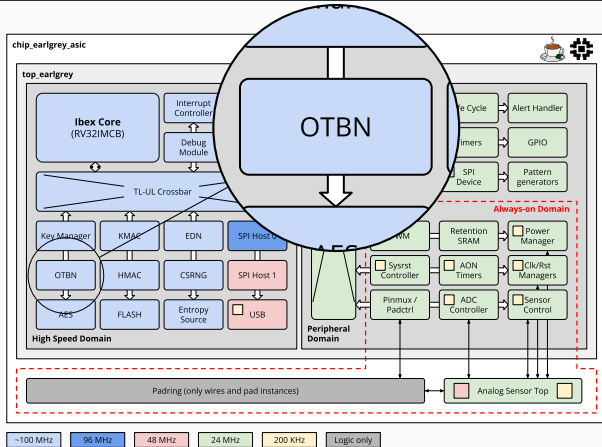


Figure 1: Overview OpenTitan Earl Grey [Ope23]

Target: OpenTitan

- Accelerator for SHA-3 and SHAKE
- OpenTitan Big Number
 - 32 256-bit registers
 - Reduced RV32I ISA
 - “Generic” big-number ISA for ECC & RSA

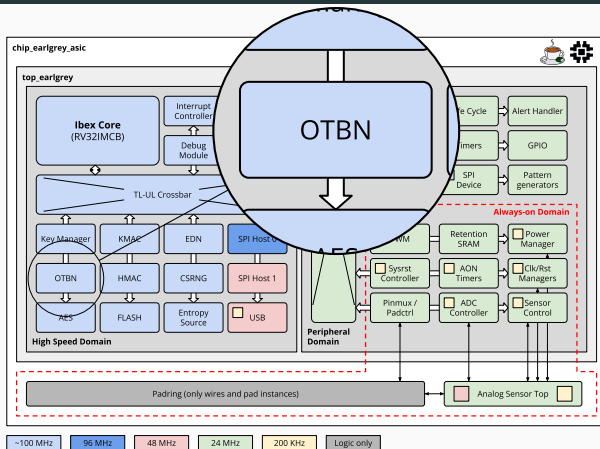
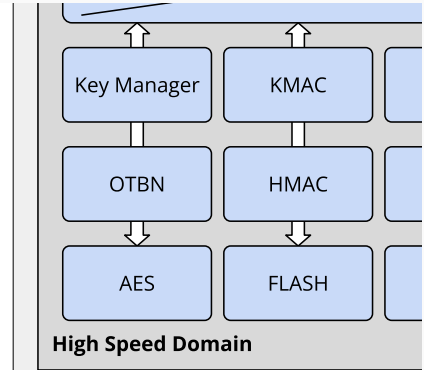


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Our Contributions

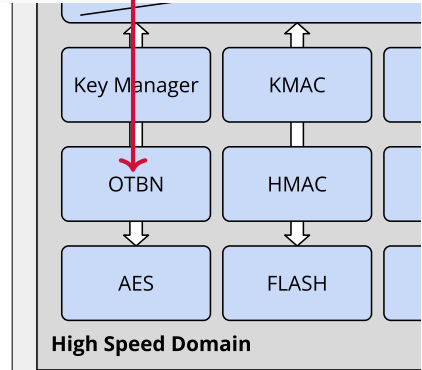


Our Contributions

1. First implementation of **ML-KEM** and **ML-DSA** on unmodified^a OTBN

^aEnlarged memories required

ML-KEM & ML-DSA

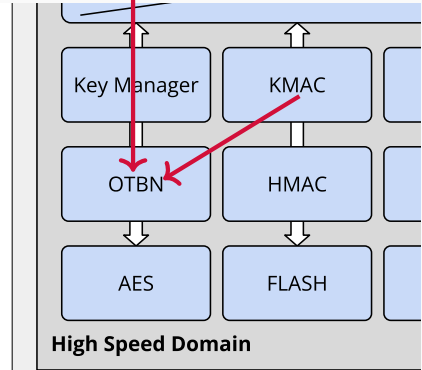


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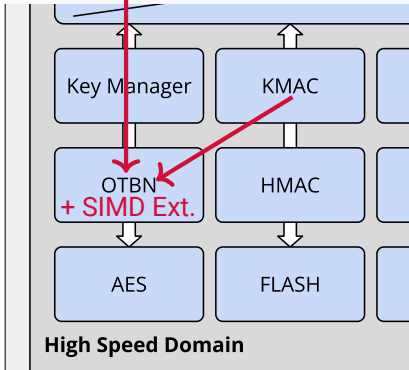


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1. First implementation of **ML-KEM** and **ML-DSA** on unmodified^a OTBN
2. Interface to **KMAC** accelerator
3. ISA extensions to utilize wide registers using **SIMD** on OTBN
 - Applicable to ML-KEM and ML-DSA but also beyond

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ML-KEM & ML-DSA

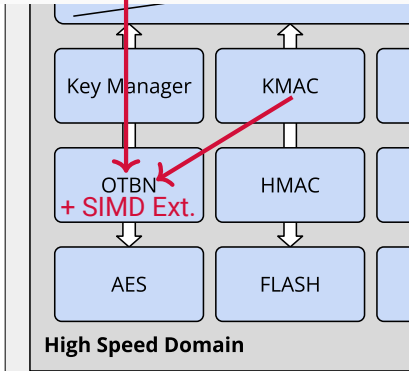


Our Contributions

1. First implementation of **ML-KEM** and **ML-DSA** on unmodified^a OTBN
2. Interface to **KMAC** accelerator
3. ISA extensions to utilize wide registers using **SIMD** on OTBN
 - Applicable to ML-KEM and ML-DSA but also beyond
4. Hardware implementation maximizing **resource-sharing** with pre-existing components

^aEnlarged memories required

ML-KEM & ML-DSA



Implementation: High Level Overview

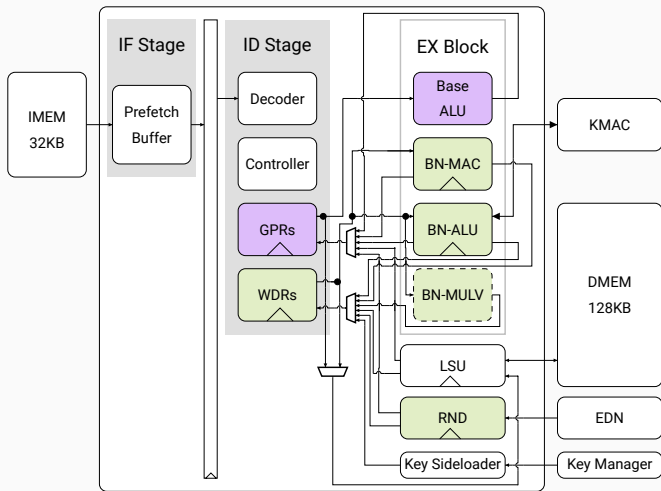


Figure 2: Simplified diagram of the modified OTBN pipeline.

Implementation: High Level Overview

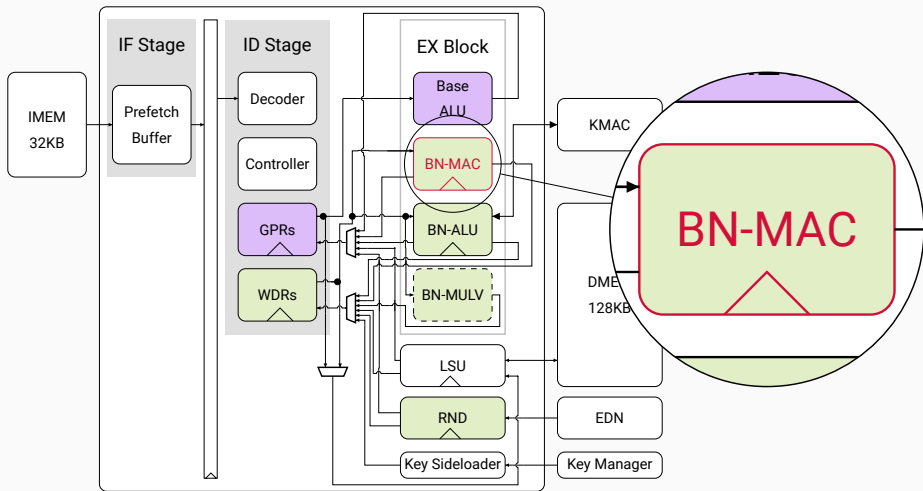


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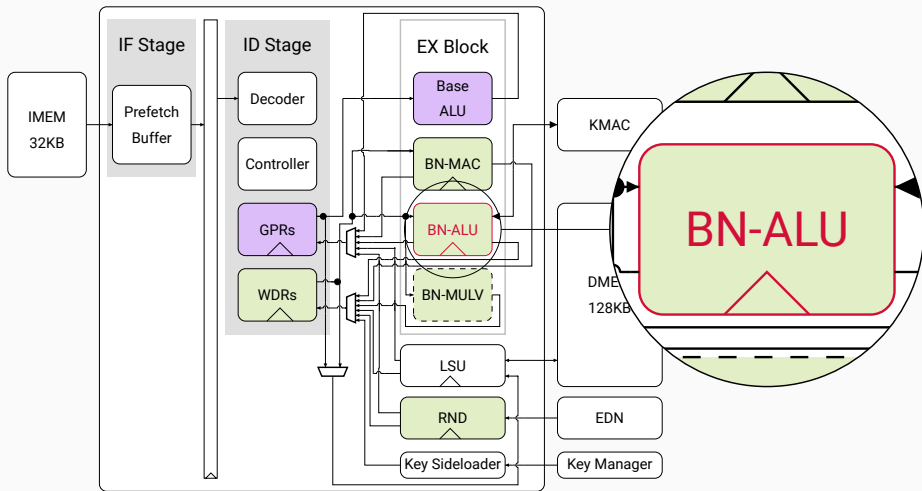


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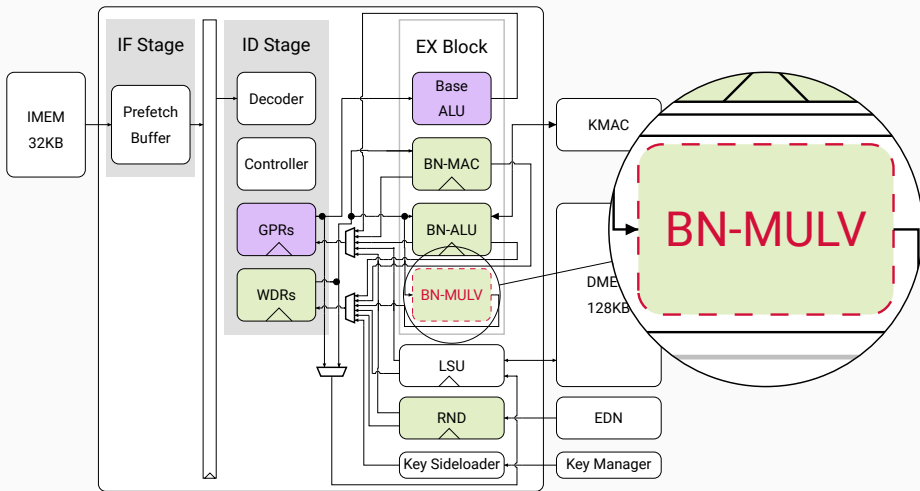
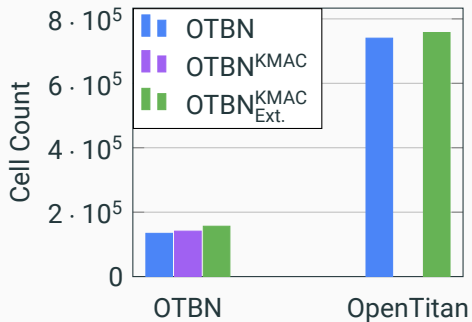


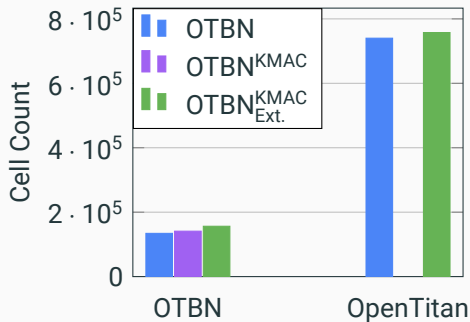
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Results

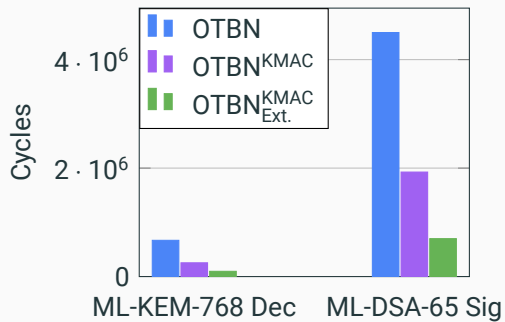


- Cell count-increase of **<17%** for OTBN
- Cell count-increase of **<3%** for OpenTitan

Results

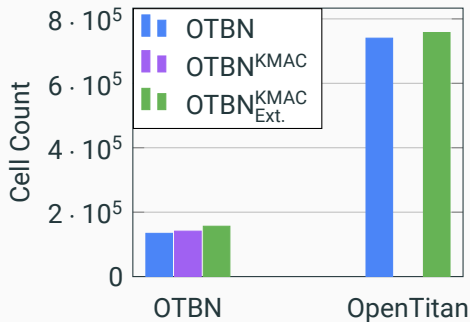


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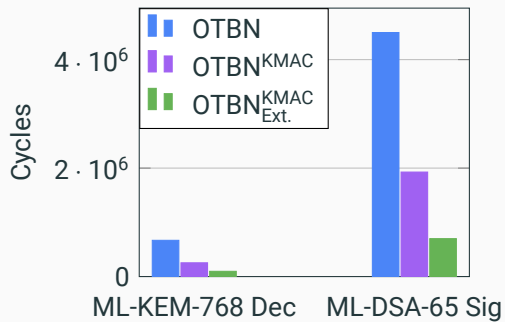


- Target Freq.: **6–9×** speedup for ML-**{KEM,DSA}**
- Max. Freq: **4–6×** net-speedup for ML-**{KEM,DSA}**

Results



- Cell count-increase of **<17%** for OTBN
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- Target Freq.: **6–9×** speedup for ML-{KEM,DSA}
- Max. Freq: **4–6×** net-speedup for ML-{KEM,DSA}

→ ML-KEM-768 Dec in 0.8 ms, ML-DSA-65 Sig in 7 ms @ Target Freq.

OpenTitan is Going PQC



ia.cr/2024/1192



[github.com/PQC-OpenTitan/
towards-ml-kem-and-
ml-dsa-on-opentitan](https://github.com/PQC-OpenTitan/towards-ml-kem-and-ml-dsa-on-opentitan)



opentitan.org

References

- [App20] Signal App. ***Signal Logo***.
<https://github.com/signalapp/Signal-Desktop/blob/87d6a55ce87a3f09e34da1dbc9e69f400ac7e32d/images/signal-logo.svg>. 2020.
- [Clo25] Inc. Cloudflare. ***Press Kit***. <https://www.cloudflare.com/press-kit>. 2025.
- [Com22] Zoom Video Communications. ***The logo of Zoom Video Communications - company responsible for the Zoom software***.
<https://zoom.us/docs/doc/Zoom-logos.zip>. 2022.

Bibliography ii

- [Cor19] Mozilla Corporation. **Firefox Logo**.
<https://phabricator.services.mozilla.com/D41016>. 2019.
- [FSS20] Tim Fritzmann, Georg Sigl, and Johanna Sepúlveda. “**RISQ-V: Tightly Coupled Accelerators for Post-Quantum Cryptography**”. In: *IACR TCHES* 2020.4 (2020), pp. 239–280. ISSN: 2569-2925. DOI: [10.13154/tches.v2020.i4.239-280](https://doi.org/10.13154/tches.v2020.i4.239-280). URL: <https://tches.iacr.org/index.php/TCHES/article/view/8683>.
- [Gmb] Nitrokey GmbH. **Nitrokey 3A NFC Hacker**. <https://shop.nitrokey.com/de/shop/nk3anh-nitrokey-3a-nfc-hacker-404>.
- [Goo22] Google. **Google Chrome logo**.
<https://www.google.com/chrome/static/images/chrome-logo.svg>. 2022.

Bibliography iii

- [Ope23] OpenTitan Team. **Datasheet - OpenTitan Documentation**. 2023. URL: <https://opentitan.org/book/doc/introduction.html> (visited on 12/10/2023).
- [Res21] IBM Research. **IBM Quantum System One in Germany for the Fraunhofer Institute**. https://www.flickr.com/photos/ibm_research_zurich/51248690716/. 2021.
- [SO25] Cyrus Stoller and Miguel Osorio. **Fabrication begins for production OpenTitan silicon**. <https://opensource.googleblog.com/2025/02/fabrication-begins-for-production-opentitan-silicon.html>. 2025.